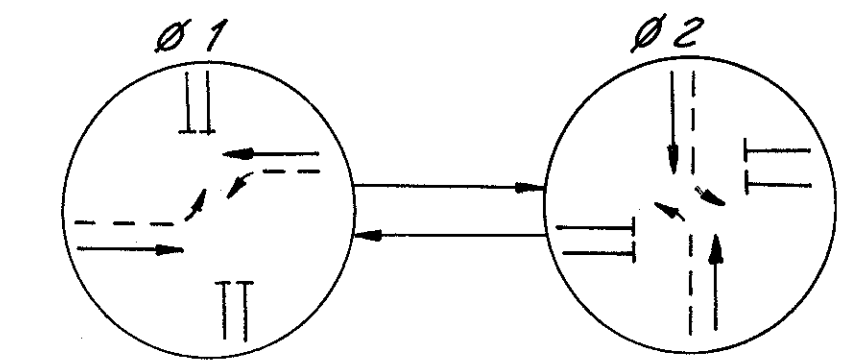


SIGNAL STRAIN POLE ELEVATION

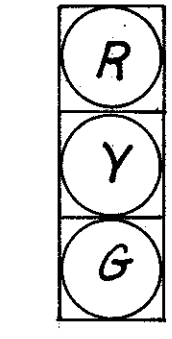
Sequence	Controller Data					
	Phase					
	1	2	1	2	Flash	
①	R/W	C.L.	R/W	C.L.	Flash	
②	G	Y	R	R	R	Y
③	R	R	R	G	Y	R
④	R	R	R	G	Y	R
⑤	G	Y	R	R	R	Y
⑥	G	Y	R	R	R	Y
⑦	G	Y	R	R	R	Y
⑧	G	Y	R	R	R	Y
⑨	R	R	R	G	Y	R

All Signals are 8"



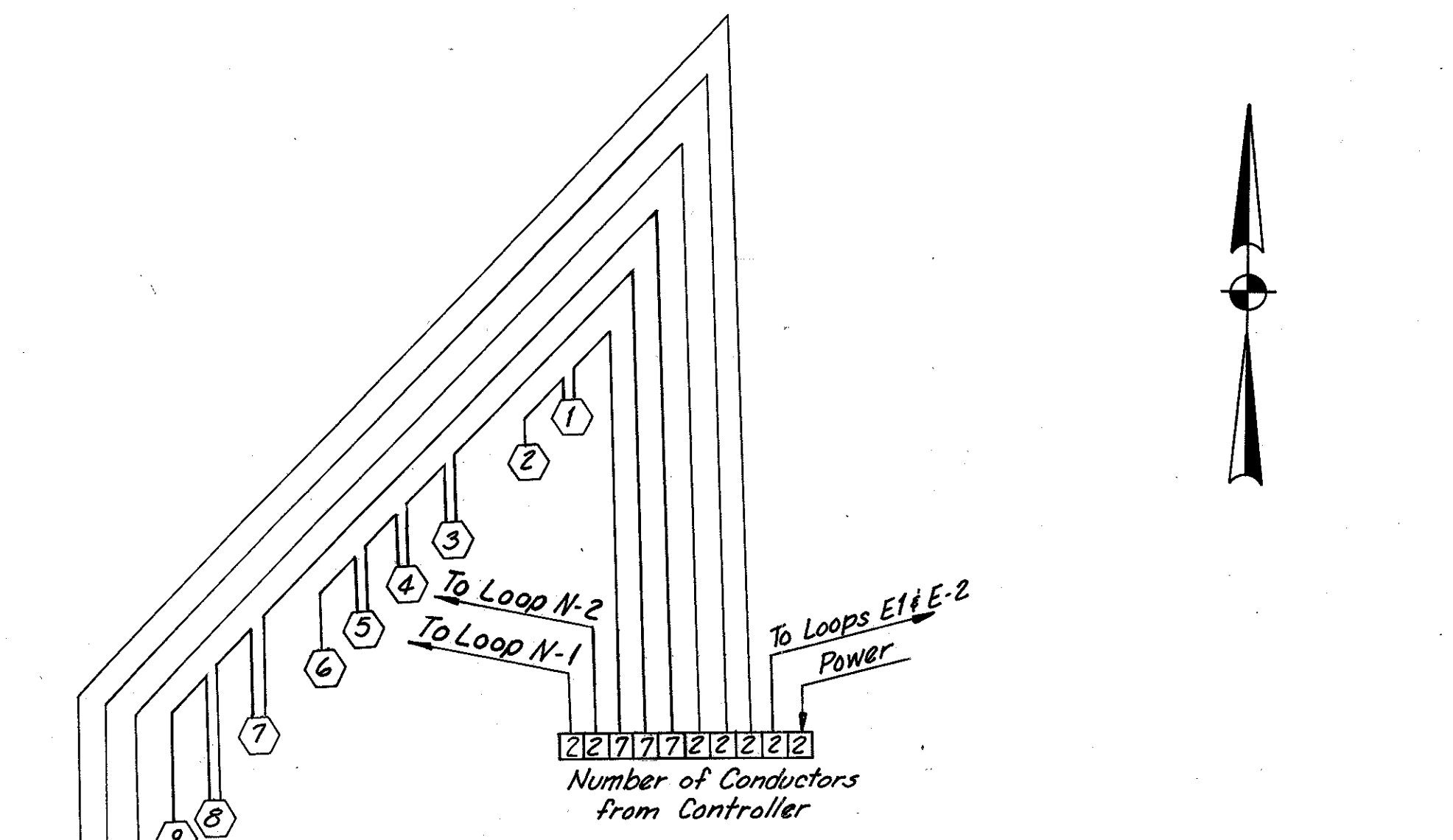
PHASING SEQUENCE SCHEMATIC

Detector and Amplifier Data				
Det.	Size	Type	Phase	Delay
E-1	20'x6'	Pulse	1	
E-2	8'x6'	"	1	
W-1	8'x6'	"	1	
W-2	8'x6'	"	1	
N-1	40'x6'	Presence	2	5 sec.
N-2	25'x6'	"	2	3 sec.
S-1	25'x6'	"	2	3 sec.
S-2	40'x6'	"	2	5 sec.

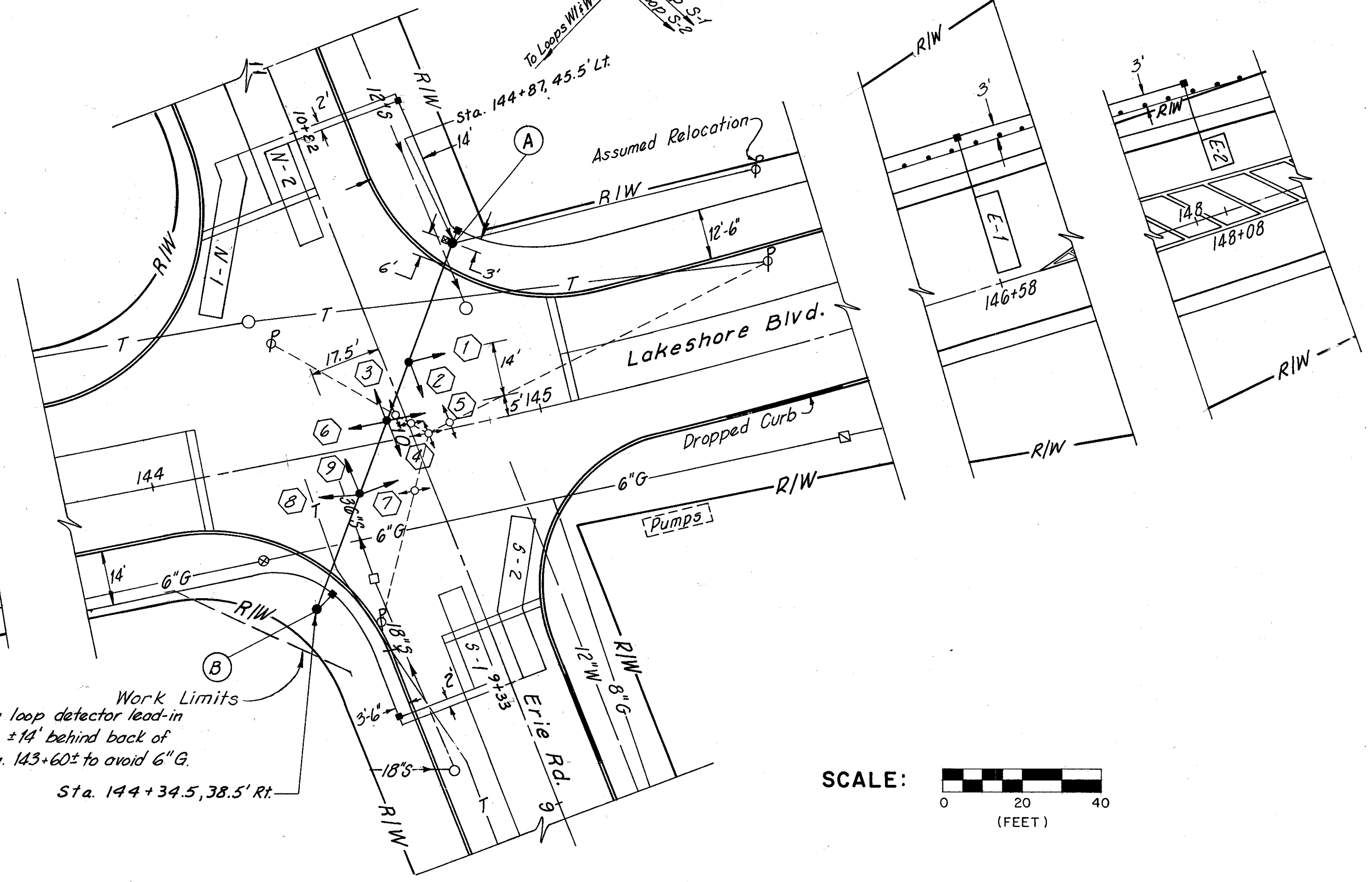
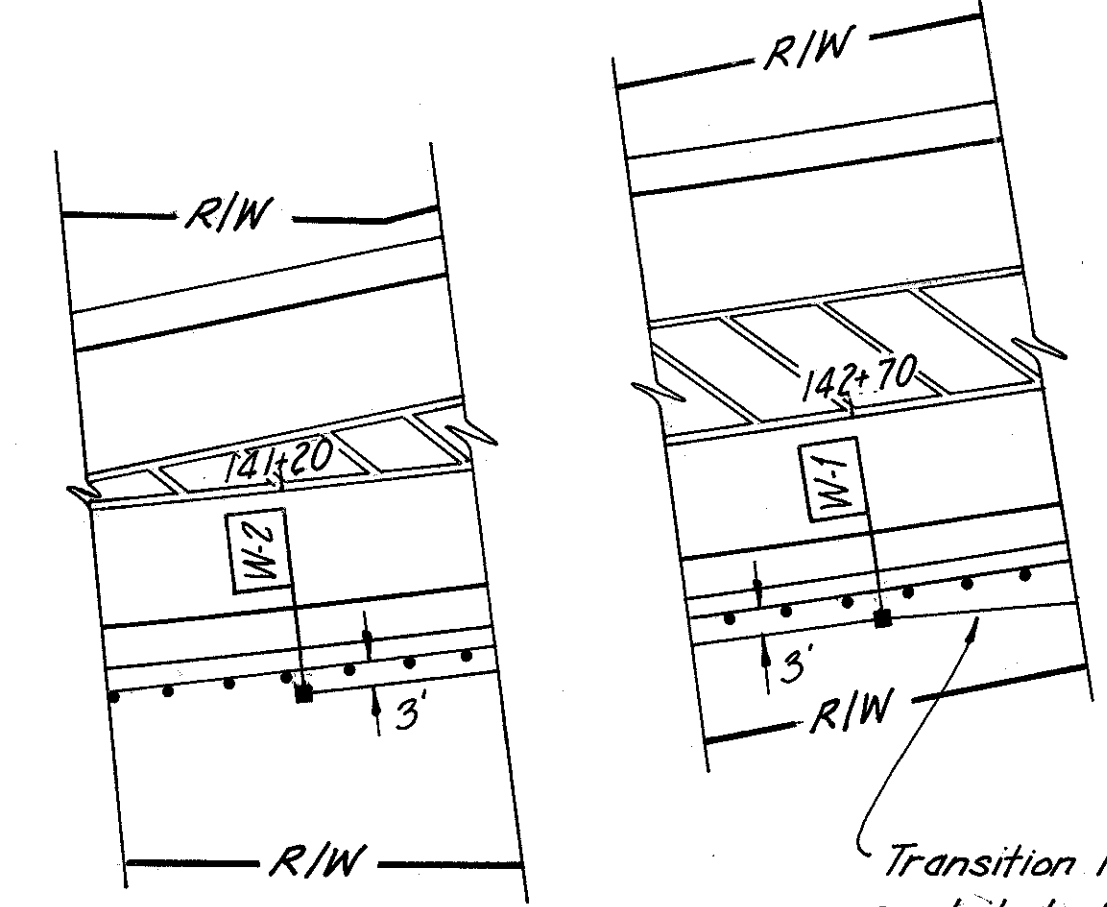


ALL SIGNALS ARE 8"

Interval	Phase	
	1	2
Min. Green	8	8
Passage	2.9	2.7
Sec Per Actv.	-	-
Time Before Reduction	-	-
Time to Reduce	-	-
Min. Gap	-	-
Max. Green	44	30
Yellow Change	4	3.6
Red Clearance	1.3	1.8
Recall	On	off
Memory	On	off



WIRING DIAGRAM



Work Limits
 Transition loop detector lead-in
 conduit to ±14' behind back of
 curb @ Sta. 143+60± to avoid 6" G.
 Sta. 144+34.5, 38.5' Rt.



SIGNAL PLAN